WRITTEN INQUIRIES AND RESPONSES TO THE FOLLOWING DIRECT QUOTE (DQ)

Bid Number: EQ25005 Dilution Refrigerator DQ

Purchasing Agent: Elevate Quantum
E-Mail: RFQ@ElevateQuantum.org

BID DUE DATE AND TIME: 24, January, 2025, 5:00 PM (Mountain Time)

SCHEDULE OF ACTIVITIES	DATE	TIME (MT)
DQ Publication	07, January, 2025	
Written Inquiry Deadline	13, January, 2025	3:00 PM
Publication of Responses to Written Inquiries	No later than 17, January, 2025	5:00 PM
DQ Submission Deadline	24, January, 2025	5:00 PM

QUESTIONS:

The following questions have been submitted from vendors related to this DQ and are being provided with responses to all interested parties

1. **Is this a request for a quote for DRs with 200 rf lines equipped with SMA connectors? Or is this a requirement for DRs that have the capacity to install 200 rf lines at a later date? Relatedly, would high-density connectors other than SMA be considered, or must it be 200 rf lines with SMA connectors?**
    a. This requirement is for the capacity to add up to 200 rf lines as a max capacity but not necessarily delivered equipped with 200 rf lines. High-density connectors other than SMA, such as flex lines are acceptable as well to include in the bid.
2. **Is the auxiliary equipment requirement a request for a DR with a 5-qubit chip? Or is the requirement that the DRs include the necessary wiring to enable the addition of a user-supplied 5-qubit chip?**
    a. One of the three DR's quoted should include the necessary wiring to operate a 5-qubit chip, referenced as the "5 qubit wiring tree". The 5-qubit chip should also be provided as a part of the bid. If the chip is sourced from another vendor please include vendor specs related to the chip as well as confirmation that the chip is compatible with the DR's in the bid.
3. **Can you define "qubit" and "array" in this context?**
    a. The 5 Qubit array refers to a 5 qubit chip (QPU) that should be included in the bid. As mentioned above If the chip is sourced from another vendor please include vendor specs related to the chip as well as confirmation that the chip is compatible with the DR's in the bid.
4. **What are the input and output line requirements per qubit?**
    a. Input and output line requirements per qubit will need to match the third party chip selected and included in the bid.

5. **Do you have a specification for the vibration isolation requirement?**
    a. We do not have a specification for vibration isolation, however, in order to compare bids please include specifications and expected performance related to vibration isolation. If there are upgrades or add-ons related to different levels of vibration isolation, please include those as separate line items so they can be evaluated.